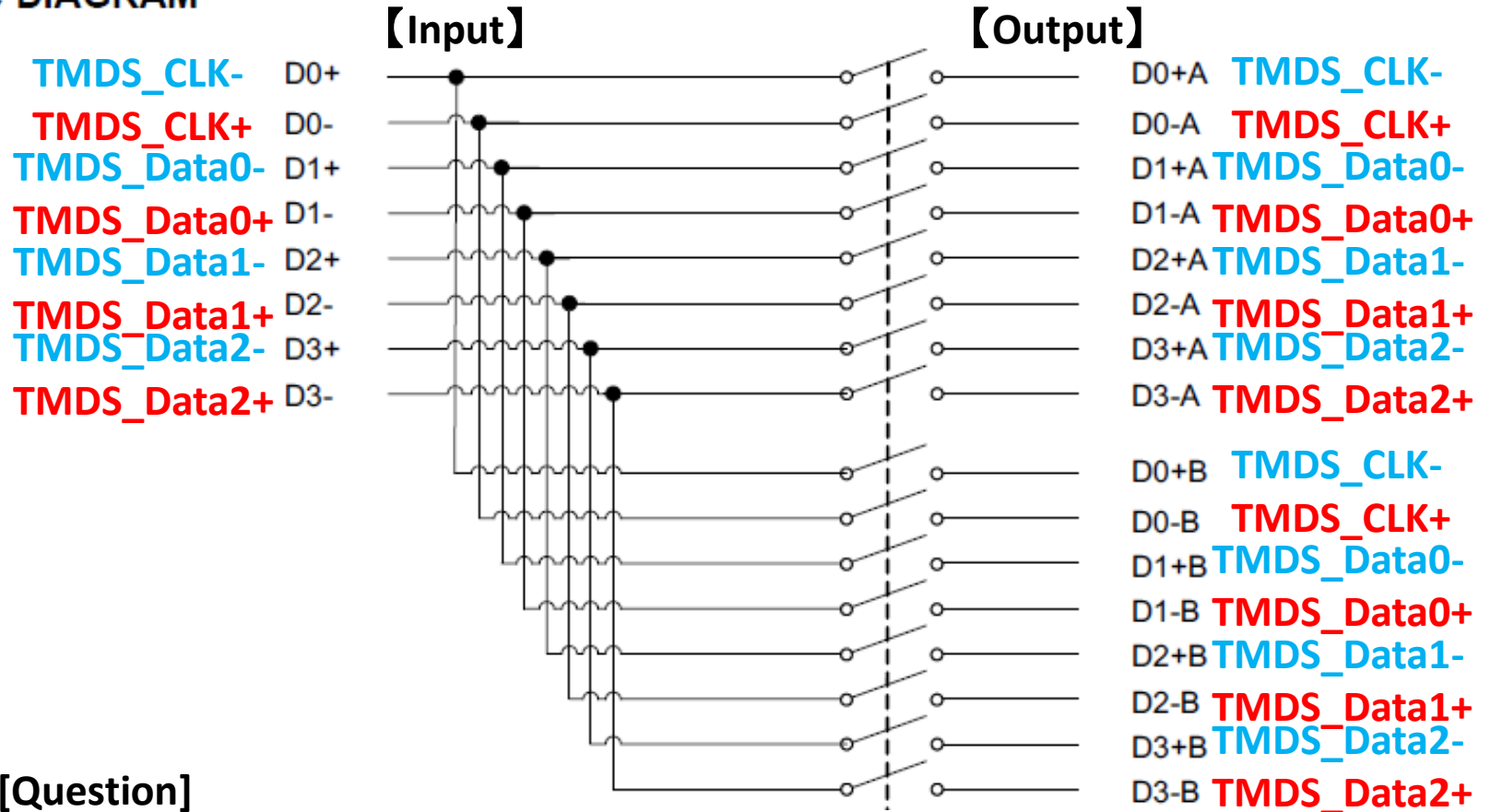


TS3DV642

LOGIC DIAGRAM



[Question]

Is TS3DV642 available on the following conditions?

They use TS3DV642 as DEMAX(1:2).

<Conditions>

- Input TMDs_CLK- or TMDs_Data- (negative signal) into D+ pin (5/7/10/12 pin)
- Input TMDs_CLK+ or TMDs_Data+(positive signal) into D- pin (6/8/11/13 pin)