			-					_
_	1	2	3	4	5	The Stackup Legend below this is sta	***	٦
						If you change the stackup, update th	he Legend.	
						Layer Stack Up Detail for: Ref_Design.PcbDc	ос	
						Top Solder Mask ("GTS) Solder R		
						Top Layer		
						Bottom Solder Mask ("GBS) Solder R	Resist	
A						DESIGN INFORMATION		1 A
						BOARD SIZE (REFER ALSO ARRAY/PANEL PROFILING II	INFORMATION)	1
						Number of Layers : 2		
						MIN. TRACK WIDTH: 8 ML		
						MIN. CLEARANCE: 8 ML MIN. VIA PAD SIZE: 24 ML		
Ш						MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL		
						PER IPC-D-275 CLASS 2 LEVEL C	FC	
						REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLE	ES +/- 3 MIL	ł
						MATERIAL:		
В						FR-408 X FR-4 High Tg OTHER THICKNESS: X 62 MIL (1.6mm) +/-10% OTHER		1
						TOLERANCE: X ANSI PC-6012 TYPE 3 CLASS		
						OTHER +/-	0.2	
						BOW & TWIST: X ANSI IPC-6012 TYPE 3 CLASS	S 2	В
						OTHER +/-	_	
						COPPER THICKNESS (FINISHED):		
						OUTER: X 1.4ML (1oz) 2ML (1.4oz)	2.8ML (2oz)	
						INNER SIGNAL: X 1.4MIL (1oz) 2.8MIL (2oz) DRILLING:	N/A	
						REFERENCE: X AS SHOWN X NC_DRILL FILES	S	
						PTH MIN COPPER THICKNESS: X 1MIL OTHER		
П						BOARD FINISH: SILKSCREEN: X TOP X BOTTOM		
						SILKSCREEN COLOR: X WHITE OTHER		
						SOLDER RESIST COLOR:		
						X GREEN BLUE OTHER _		
						SURFACE FINISH: X IMMERSION GOLD (ENIG)	ENEPIG	1
						MM. TIN/SILVER OR EQUIV OTHER		
						ARRAY/PANEL: CUT AND TRM PER MECH LAYE	ER 1	
						N.C. ROUTE X V. SCORE		
						CERTIFICATION: MATERIALS AND WORKMANSHIP FOR AL TO MEET OR EXCEED THE REQUIREMENT	LL PCBS	
						X ANSI IPC-A-600F CLASS -> 1 X 2	3	
						X UL 94V-0 X RoHS OTHER I		
						ADDITIONAL REQUIREMENTS:		
						MICROSECTION: YES BARE BOARD ELEC. TEST: NONE X REQUIRED	DEB OBDER	
\mathbf{H}						MANUFACTURER'S UL: RAIL METAL X SIL	K OUDEK	H
								4
	Oth Control of the Co					TEXAS INSTRUMENTS		
						Instruments		
	INSPREMENTS					PROJECT TITLE:		1
	الك					NFC Authentication		
						DESIGNED FOR:		
D	dash					Public Release		- D
	ASSEMBLY VARIANT: [No Variations] :TNARAU YJ9M328A RE				ISE4003 E1 PCB. PcbDoc			
	ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: ISE4003 REV: E1 S		uments (TI) and/or its licensors do not warrant the accuracy		ENGINEER: LAYOUT BY:		1
	I		or any info	mation contained therein. TI and/or its licensors do not war ations, will be suitable for your application or fit for any		Evan Cornell Krypton		_
-	Multilayer Composite Prin	t GENERATED : 10/22/2014 8:55:38 AM	an implemen	ation. TI and/or its licensors do not warrant that the designalidate and test your design implementation to confirm the s	n is production worthy. You should	ALTIUM DESIGNER VERSION: SCALE: 1.00 10.0.0.27009		
	nartirager composite Prin	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	z z	A	gates ranctionality for god applications	10:0:0:27000	<u> </u>	┙