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;
;
; Interrupt Service Routine Table (ISR Table) for C6747
;
; Setup ISR Fetch Packet Tables to Branch to Interrupt Service Routines (ISR)
; Fetch Packets are 8 Words (Uint32) long and aligned on 8 Word boundaries
; ISR Table Must be Aligned on 256 word (32bit) boundary !
;
        .sect   .isrtable
        .global _isrtable
        .ref _c_int00
        .ref _nmi_isr
        .ref _int4_isr
        .ref _int5_isr
        .ref _int6_isr
        .ref _int7_isr
        .ref _int8_isr
        .nocmp
        .asmfunc

        ; ************************************************************
        ; Branch to start of RESET
        ; ************************************************************
_isrtable:
        stw  .D2 B0, *--B15[1]                  ; Save B0 ==> **SP[-1]
        mvkl .S2 _c_int00, B0                   ; Addrl _c_int00 ==> B0
        mvkh .S2 _c_int00, B0                   ; Addrh _c_int00 ==> B0
        B .S2 B0                                ; branch to _c_int00
        ldw  .D2 *B15++[1],B0                   ; **SP ==> Restore B0
        nop  4                                  ; NOP 4
        nop
        nop
        ;
        ; ************************************************************
        ; Branch to start of Non-Maskable Interrupt (NMI)
        ; ************************************************************
nmi:
        stw  .D2 B0, *--B15[1]                  ; Save B0 ==> **SP[-1],  SP= B15
        mvkl .S2 _nmi_isr, B0
        mvkh .S2 _nmi_isr, B0
        B .S2 B0                                ; branch to _nmi_isr
        ldw  .D2 *B15++[1],B0                   ; **SP ==> Restore B0
        nop   4
        nop
        nop
        ;
        ; ************************************************************
        ; Branch to start of RESERVED1 ISR
        ; ************************************************************
reserved1:
        stw  .D2 B0, *--B15[1]                  ; Save B0 ==> **SP[-1],  SP= B15
        mvkl .S2 reserved1, B0
        mvkh .S2 reserved1, B0
        B .S2 B0                                ; branch to reserved1
        ldw  .D2 *B15++[1],B0                   ; **SP ==> Restore B0
        nop   4
        nop
        nop
        ;
        ; ************************************************************
        ; Branch to start of RESERVED2  ISR
        ; ************************************************************
reserved2:
        stw  .D2 B0, *--B15[1]                  ; Save B0 ==> **SP[-1],  SP= B15
        mvkl .S2 reserved2, B0
        mvkh .S2 reserved2, B0
        B .S2 B0                                ; branch to reserved2
        ldw  .D2 *B15++[1],B0                   ; **SP ==> Restore B0
        nop  4
        nop
        nop
        ;
        ; ************************************************************
        ; Branch to start of INT4  ISR
        ; ************************************************************
int4:
        stw  .D2 B0, *--B15[1]                  ; Save B0 ==> **SP[-1]
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```
        mvkl .S2 _int4_isr, B0
        mvkh .S2 _int4_isr, B0
        B .S2 B0                                ; branch to int4 isr
        ldw  .D2 *B15++[1],B0                   ; **SP ==> Restore B0
        nop    4
        nop
        nop
        ;
        ; ************************************************************
        ; Branch to start of INT5 ISR
        ; ************************************************************
int5:
        stw  .D2 B0, *--B15[1]                  ; Save B0 ==> **SP[-1]
        mvkl .S2 _int5_isr, B0
        mvkh .S2 _int5_isr, B0
        B .S2 B0                                ; branch to int5 isr
        ldw  .D2 *B15++[1],B0                   ; **SP ==> Restore B0
        nop  4
        nop
        nop
        ;
        ; ************************************************************
        ; Branch to start of INT6 ISR
        ; ************************************************************
int6:
        stw  .D2 B0, *--B15[1]                  ; Save B0 ==> **SP[-1]
        mvkl .S2 _int6_isr, B0
        mvkh .S2 _int6_isr, B0
        B .S2 B0                                ; branch to int6 isr
        ldw  .D2 *B15++[1],B0                   ; **SP ==> Restore B0
        nop  4
        nop
        nop
        ;
        ; ************************************************************
        ; Branch to start of INT7 ISR
        ; ************************************************************
int7:
        stw  .D2 B0, *--B15[1]                  ; Save B0 ==> **SP[-1]
        mvkl .S2 _int7_isr, B0
        mvkh .S2 _int7_isr, B0
        B .S2 B0                                ; branch to int7 isr
        ldw  .D2 *B15++[1],B0                   ; **SP ==> Restore B0
        nop  4
        nop
        nop
        ;
        ; ************************************************************
        ; Branch to start of INT8 ISR
        ; ************************************************************
int8:
        stw  .D2 B0, *--B15[1]                  ; Save B0 ==> **SP[-1]
        mvkl .S2 _int8_isr, B0
        mvkh .S2 _int8_isr, B0
        B .S2 B0                                ; branch to int8 isr
        ldw  .D2 *B15++[1],B0                   ; **SP ==> Restore B0
        nop  4
        nop
        nop
        ;
        ; ************************************************************
        ; Branch to start of INT9 ISR
        ; ************************************************************
int9:
        stw  .D2 B0, *--B15[1]                  ; Save B0 ==> **SP[-1]
        mvkl .S2 int9, B0
        mvkh .S2 int9, B0
        B .S2 B0                                ; branch to int9 isr
        ldw  .D2 *B15++[1],B0                   ; **SP ==> Restore B0
        nop  4
        nop
        nop
        ;
        ; ************************************************************
        ; Branch to start of INT10 ISR
        ; ************************************************************
int10:
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```
        stw  .D2 B0, *--B15[1]                  ; Save B0 ==> **SP[-1]
        mvkl .S2 int10, B0
        mvkh .S2 int10, B0
        B .S2 B0                                ; branch to int10 isr
        ldw  .D2 *B15++[1],B0                   ; **SP ==> Restore B0
        nop  4
        nop
        nop
        ;
        ; ************************************************************
        ; Branch to start of INT11 ISR
        ; ************************************************************
int11:
        stw  .D2 B0, *--B15[1]                  ; Save B0 ==> **SP[-1]
        mvkl .S2 int11, B0
        mvkh .S2 int11, B0
        B .S2 B0                                ; branch to int11 isr
        ldw  .D2 *B15++[1],B0                   ; **SP++[1] ==> Restore B0
        nop  4
        nop
        nop
        ;
        ; ************************************************************
        ; Branch to start of INT12 ISR
        ; ************************************************************
int12:
        stw  .D2 B0, *--B15[1]                  ; Save B0 ==> **SP[-1]
        mvkl .S2 int12, B0
        mvkh .S2 int12, B0
        B .S2 B0                                ; branch to int12 isr
        ldw  .D2 *B15++[1],B0                   ; **SP ==> Restore B0
        nop   4
        nop
        nop
        ;
        ; ************************************************************
        ; Branch to start of INT13 ISR
        ; ************************************************************
int13:
        stw  .D2 B0, *--B15[1]                  ; Save B0 ==> **SP[-1]
        mvkl .S2 int13, B0
        mvkh .S2 int13, B0
        B .S2 B0                                ; branch to int13 isr
        ldw  .D2 *B15++[1],B0                   ; **SP ==> Restore B0
        nop  4
        nop
        nop
        ;
        ; ************************************************************
        ; Branch to start of INT14 ISR
        ; ************************************************************
int14:
        stw  .D2 B0, *--B15[1]                  ; Save B0 ==> **SP[-1]
        mvkl .S2 int14, B0
        mvkh .S2 int14, B0
        B .S2 B0                                ; branch to int14 isr
        ldw  .D2 *B15++[1],B0                   ; **SP ==> Restore B0
        nop  4                                  ;
        nop
        nop
        ;
        ; ************************************************************
        ; Branch to start of INT15 ISR
        ; ************************************************************
int15:
        stw  .D2 B0, *--B15[1]                  ; Save B0 ==> **SP[-1]
        mvkl .S2 int15, B0
        mvkh .S2 int15, B0
        B .S2 B0                                ; branch to int15 isr
        ldw  .D2 *B15++[1],B0                   ; **SP ==> Restore B0
        nop   4
        nop
        nop
        ;
       .endasmfunc
```