

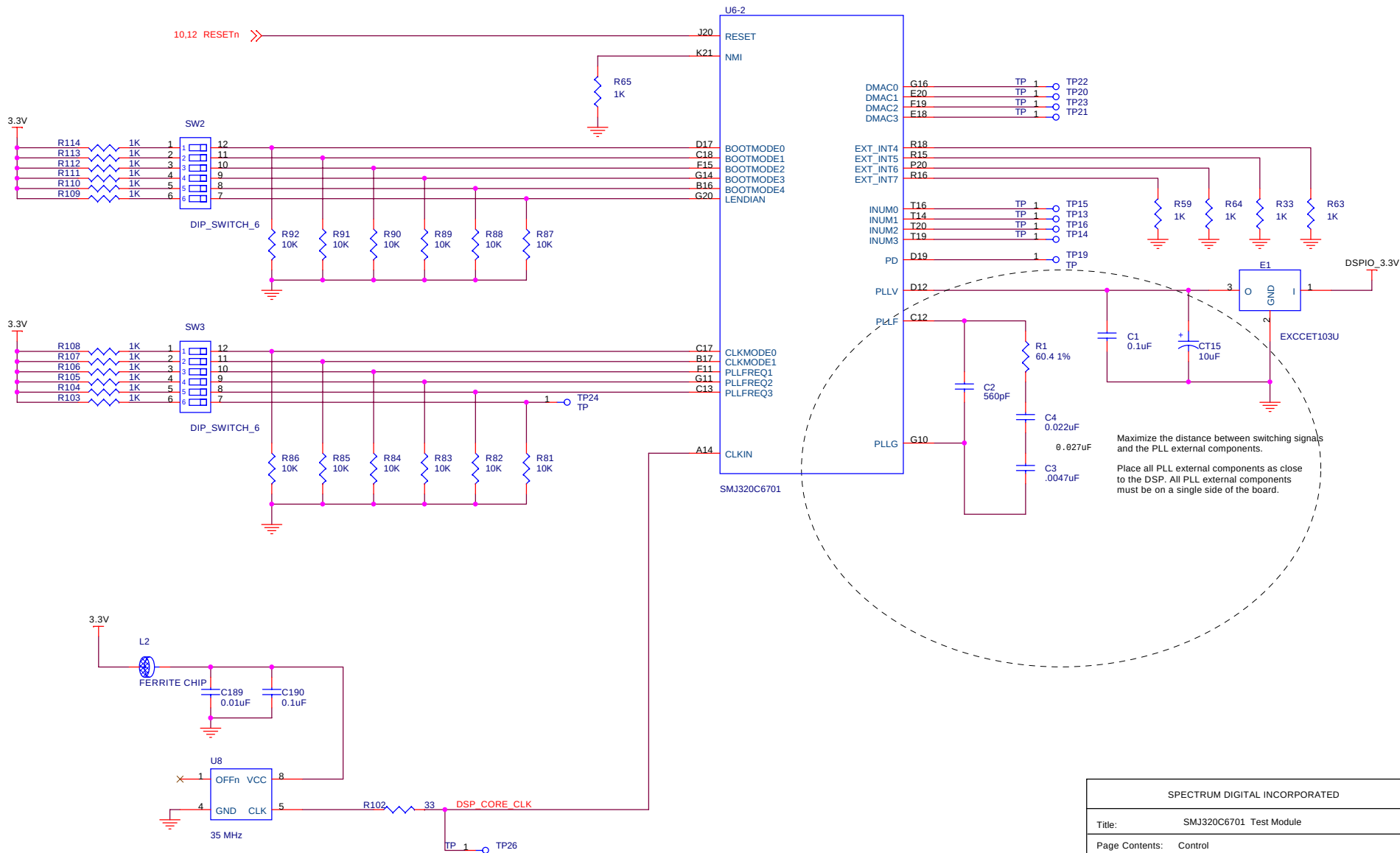
A

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	PROTOTYPE RELEASE	15-OCT-2008	

SCHEMATIC CONTENTS
01) COVER SHEET
02) 6701 CONTROL
03) 6701 EMIF
04) 6701 McBSP
05) 6701 HOST PORT
06) 6701 POWER
07) 6701 DECOUPLING CAPACITORS
08) EMULATION INTERFACE
09) SDRAM
10) FLASH
11) RS232
12) RESET CIRCUITRY
13 POWER

REVISION STATUS OF SHEETS										DWN	DATE
REV										CHK	DATE
SH										ENGR	DATE
REV	A	A	A	A	A	A	A	A		ENGR-MGR	DATE
SH	8	9	10	11	12	13	14			QA	DATE
REV	A	A	A	A	A	A	A	NEXT ASSY	USED ON	MFG	DATE
SH	1	2	3	4	5	6	7	APPLICATION		RLSE	DATE

SPECTRUM DIGITAL INCORPORATED			
Title: SMJ320C6701 Test Module			
Page Contents: TITLE SHEET			
Size:B	DWG NO	511562-00001	Revision: A
Date: Tuesday, November 04, 2008		Sheet 1 of 13	



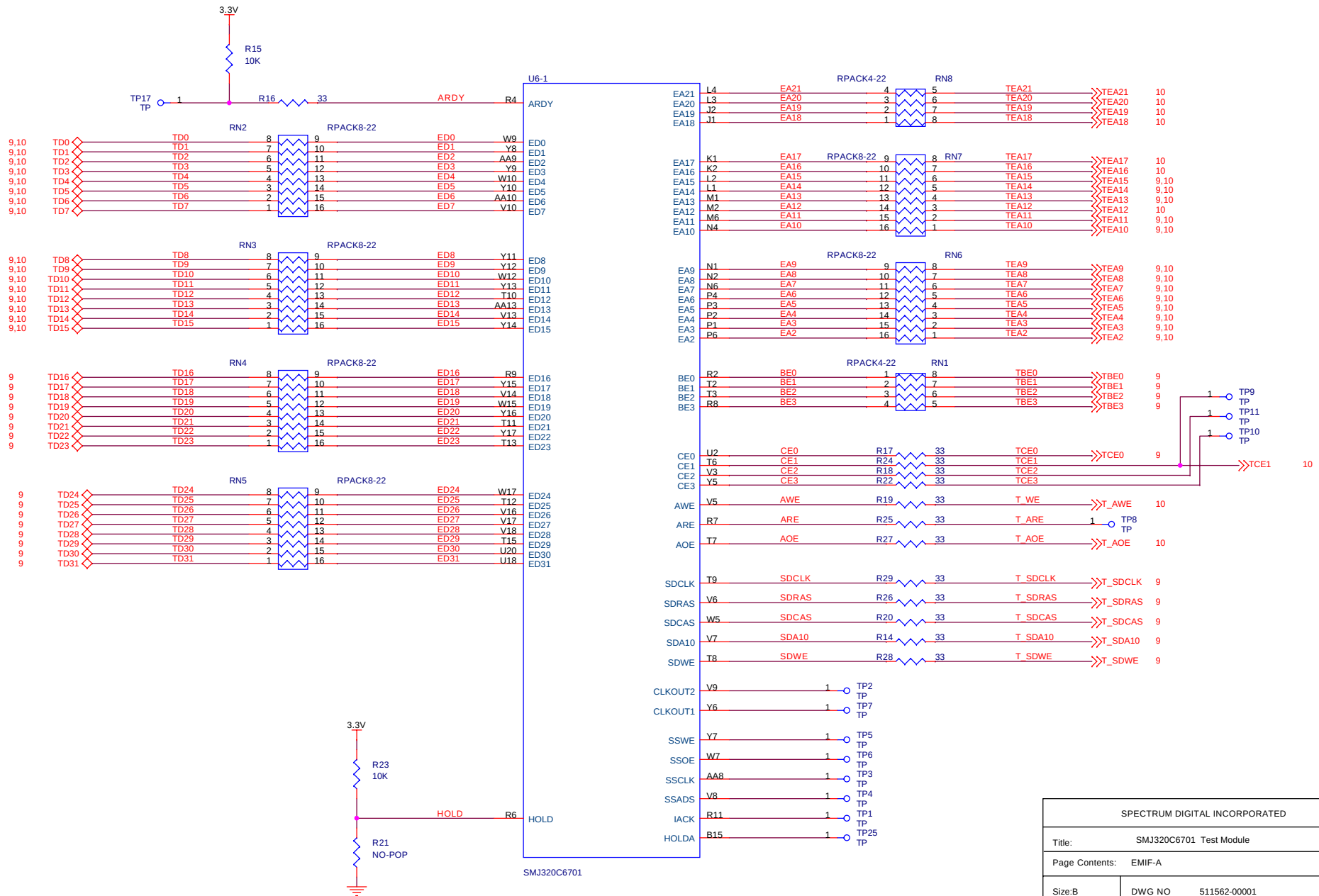
SPECTRUM DIGITAL INCORPORATED

Title: SMJ320C6701 Test Module

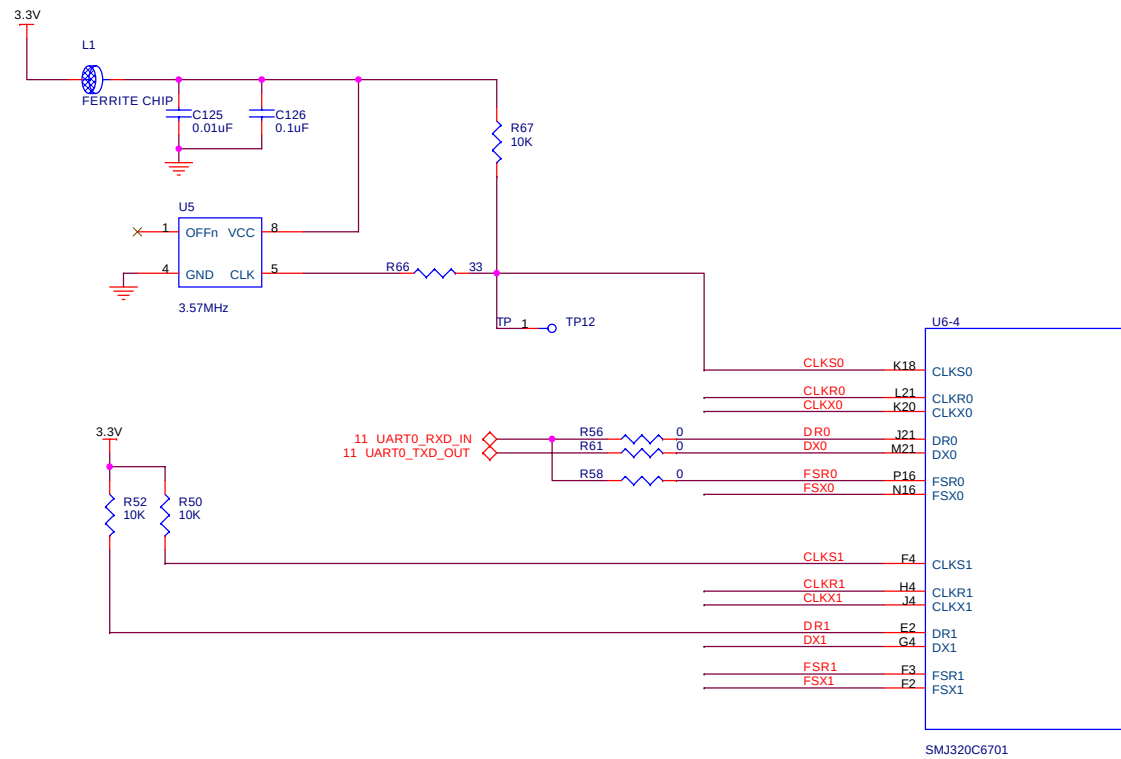
Page Contents: Control

Size: B DWG NO 511562-00001 Revision: A

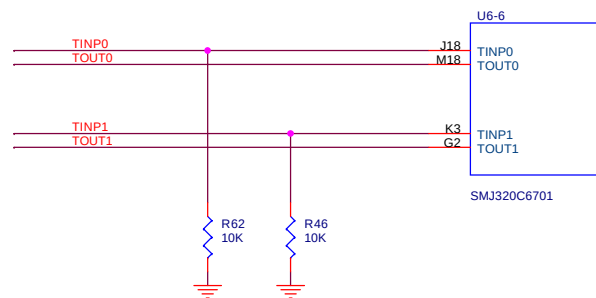
Date: Wednesday, November 05, 2008 Sheet 2 of 13



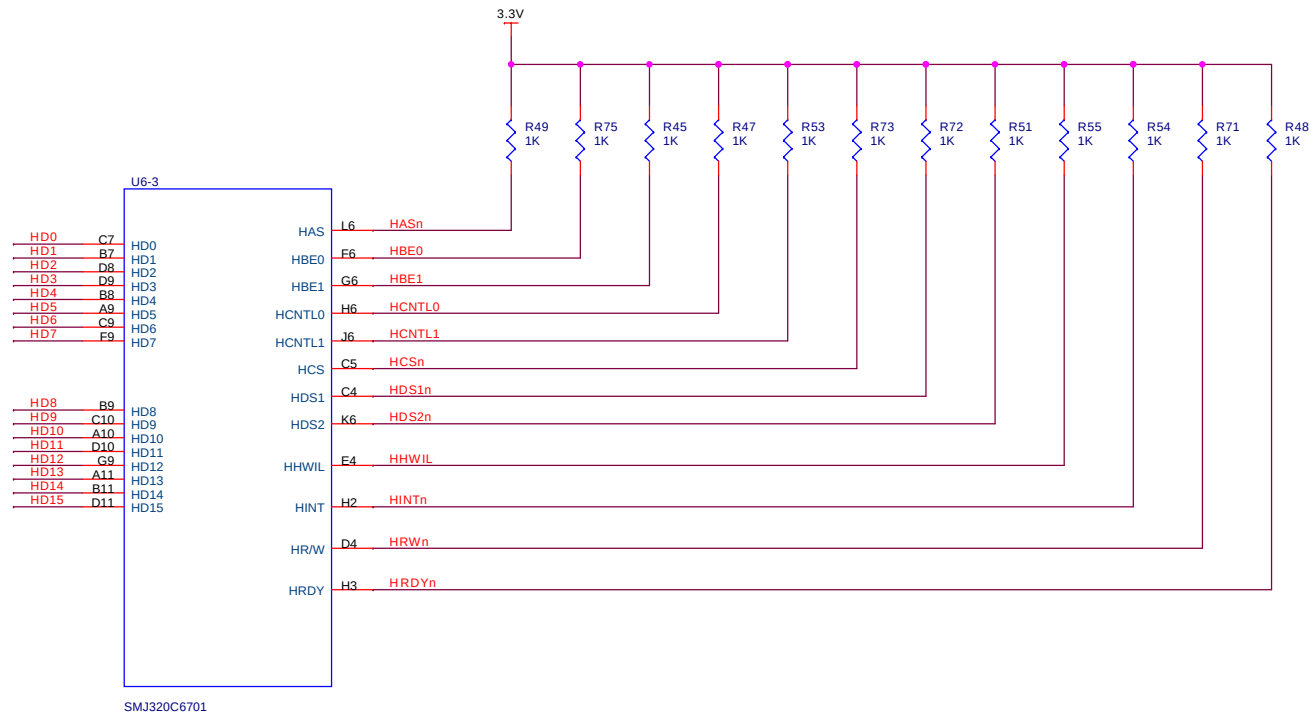
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Title: SMJ320C6701 Test Module			
Page Contents: EMIF-A			
Size: B	DWG NO	511562-00001	Revision: A
Date:	Tuesday, November 04, 2008	Sheet 3 of	13



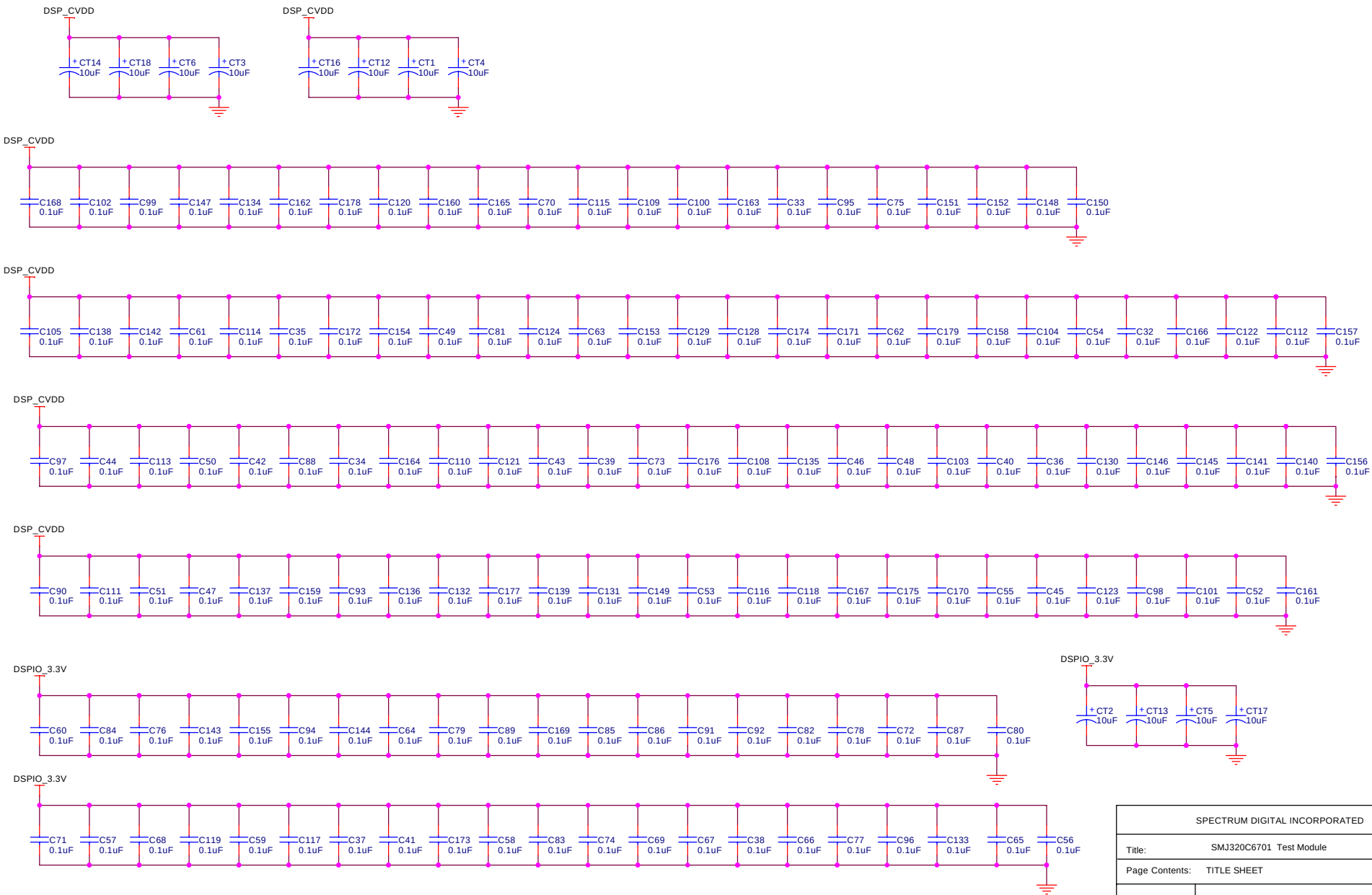
11 UART0_RXD_IN
11 UART0_TXD_OUT



SPECTRUM DIGITAL INCORPORATED			
Title: SMJ320C6701 Test Module			
Page Contents: McBSP,TIMERS			
Size:B	DWG NO	511562-00001	Revision: A
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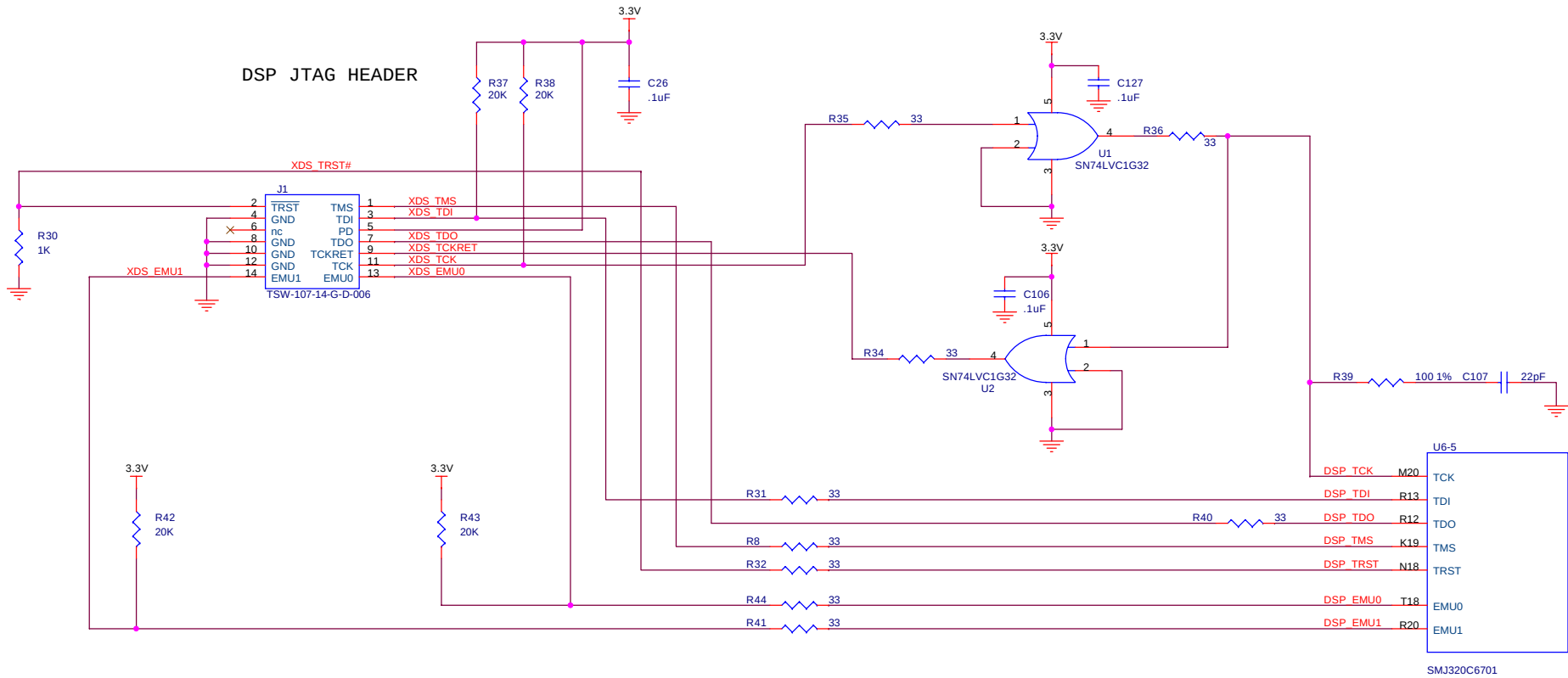


SPECTRUM DIGITAL INCORPORATED			
Title: SMJ320C6701 Test Module			
Page Contents: HOST PORT INTERFACE			
Size: B	DWG NO	511562-00001	Revision: A
Date:	Tuesday, November 04, 2008	Sheet 5 of	13

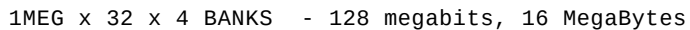


SPECTRUM DIGITAL INCORPORATED			
Title: SMJ320C6701 Test Module			
Page Contents: TITLE SHEET			
Size: B	DWG NO	511562-00001	Revision: A
Date:	Tuesday, November 04, 2008	Sheet 7 of	13

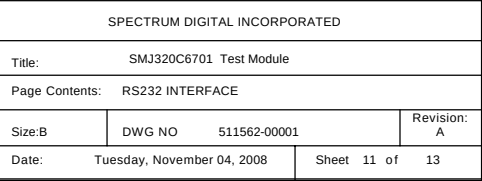
DSP JTAG HEADER

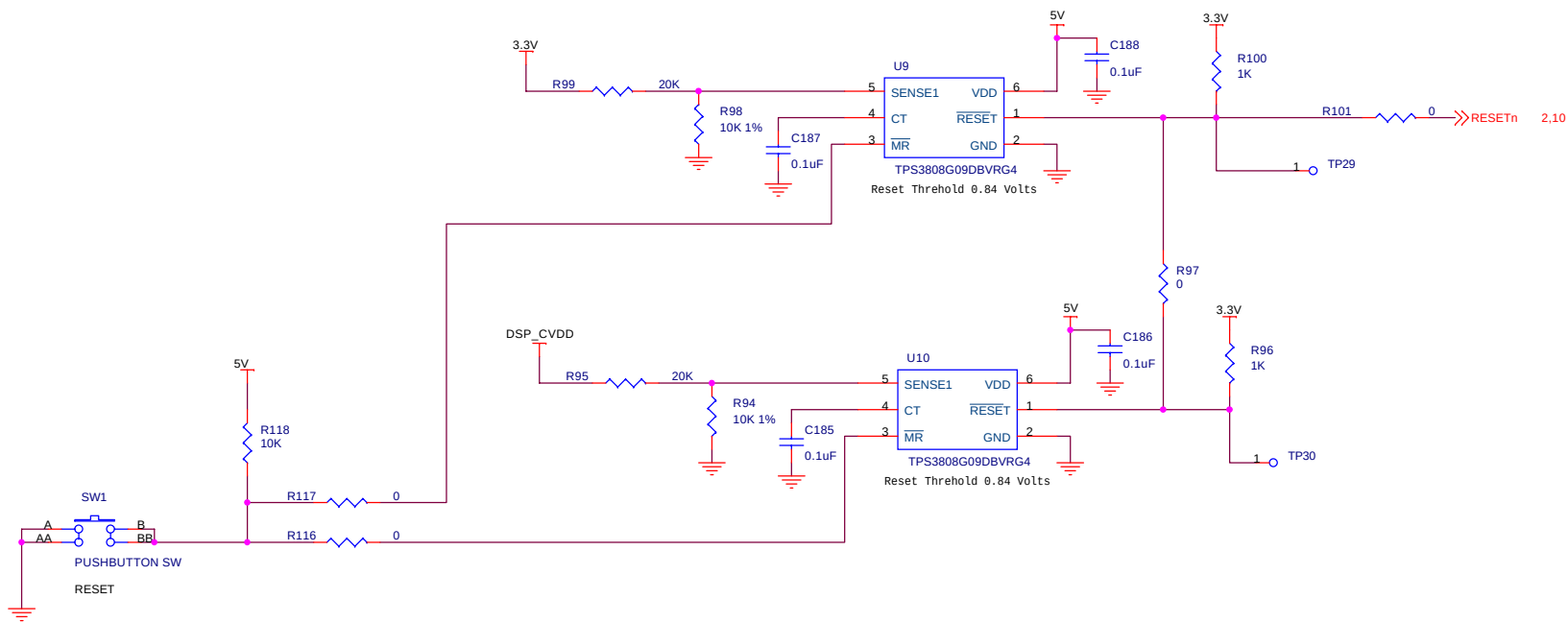


SPECTRUM DIGITAL INCORPORATED			
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Page Contents: EMULATION			
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SPECTRUM DIGITAL INCORPORATED			
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Page Contents: SDRAM			
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