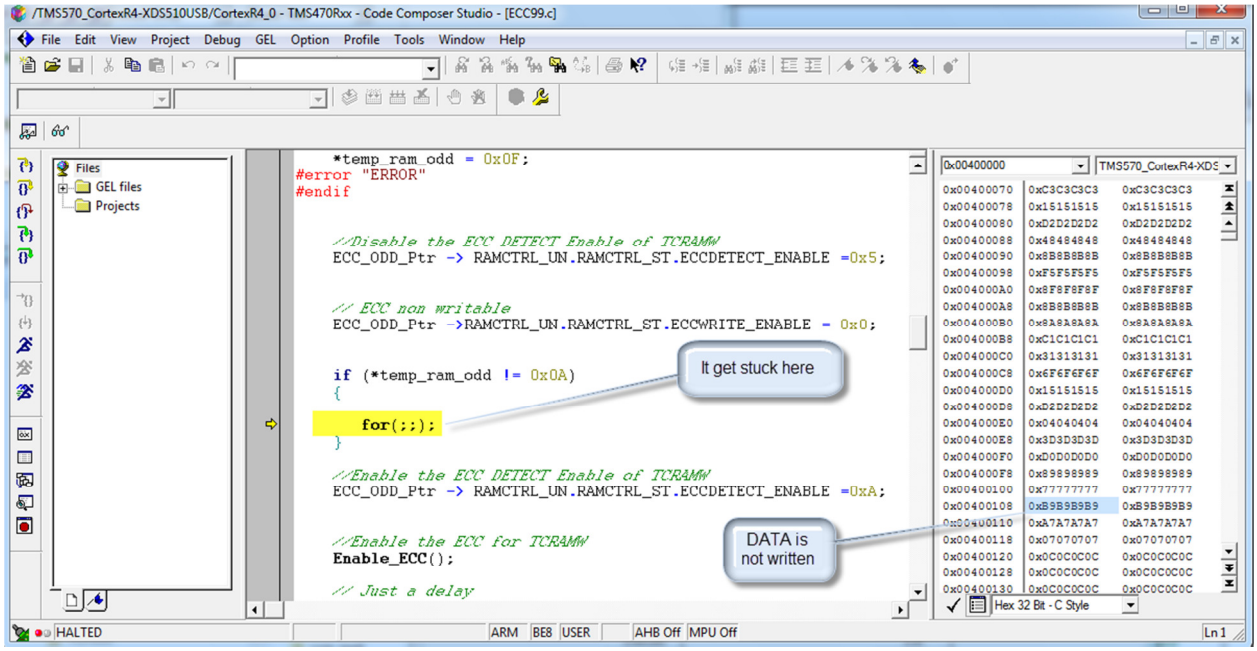
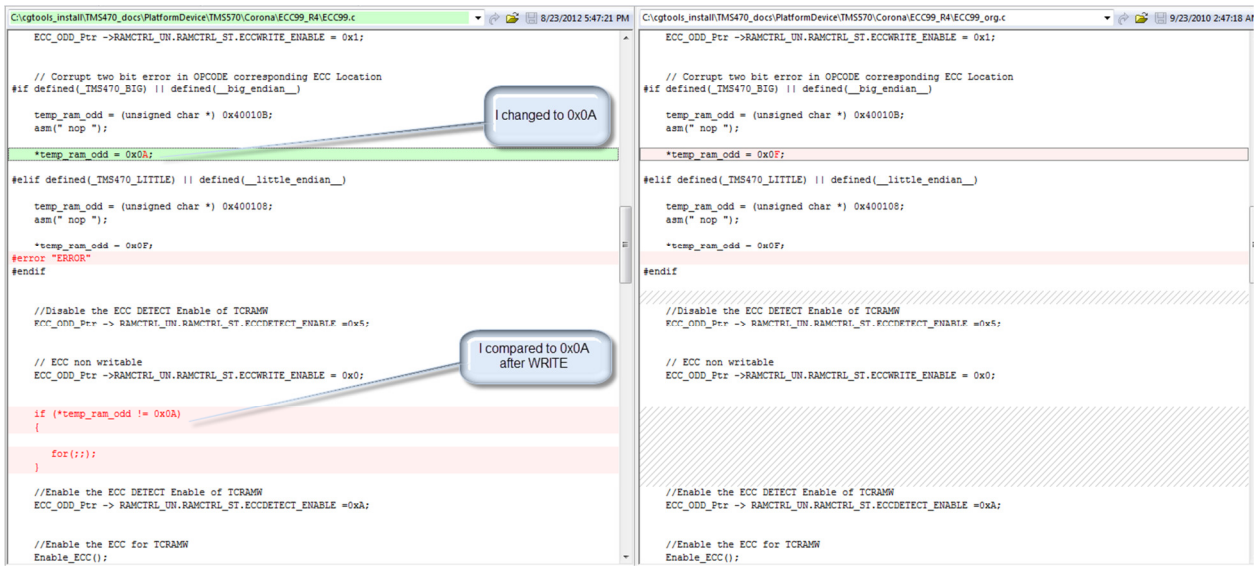

/TMS570_CortexR4-XDS510USB/CortexR4_0 - TMS470Rxx - Code Composer Studio - [ECC99.c]
File Edit View Project Debug GEL Option Profile Tools Window Help
Files
GEL files
Projects
*temp_ram_odd = 0x0F;
#error "ERROR"
#endif
//Disable the ECC DETECT Enable of TCRAMW
ECC_ODD_Ptr -> RAMCTRL_UN.RAMCTRL_ST.ECCDETECT_ENABLE =0x5;
// ECC non writable
ECC_ODD_Ptr ->RAMCTRL_UN.RAMCTRL_ST.ECCWRITE_ENABLE = 0x0;
if (*temp_ram_odd != 0x0A)
{
for(;;);
}
//Enable the ECC DETECT Enable of TCRAMW
ECC_ODD_Ptr -> RAMCTRL_UN.RAMCTRL_ST.ECCDETECT_ENABLE =0xA;
//Enable the ECC for TCRAMW
Enable_ECC();
// Just a delay
It get stuck here
DATA is not written
0x00400000
TMS570_CortexR4-XDS
0x00400070 0xC3C3C3C3 0xC3C3C3C3
0x00400078 0x15151515 0x15151515
0x00400080 0xD2D2D2D2 0xD2D2D2D2
0x00400088 0x48484848 0x48484848
0x00400090 0x8B8B8B8B 0x8B8B8B8B
0x00400098 0xF5F5F5F5 0xF5F5F5F5
0x004000A0 0x8F8F8F8F 0x8F8F8F8F
0x004000A8 0x8B8B8B8B 0x8B8B8B8B
0x004000B0 0x8A8A8A8A 0x8A8A8A8A
0x004000B8 0xC1C1C1C1 0xC1C1C1C1
0x004000C0 0x31313131 0x31313131
0x004000C8 0x6F6F6F6F 0x6F6F6F6F
0x004000D0 0x15151515 0x15151515
0x004000D8 0xD2D2D2D2 0xD2D2D2D2
0x004000E0 0x04040404 0x04040404
0x004000E8 0x3D3D3D3D 0x3D3D3D3D
0x004000F0 0xD0D0D0D0 0xD0D0D0D0
0x004000F8 0x89898989 0x89898989
0x00400100 0x77777777 0x77777777
0x00400108 0xB9B9B9B9 0xB9B9B9B9
0x00400110 0xA7A7A7A7 0xA7A7A7A7
0x00400118 0x07070707 0x07070707
0x00400120 0x0C0C0C0C 0x0C0C0C0C
0x00400128 0x0C0C0C0C 0x0C0C0C0C
0x00400130 0x0C0C0C0C 0x0C0C0C0C
Hex 32 Bit - C Style
HALTED
ARM BE8 USER AHB Off MPU Off
Ln 1


C:\cgtools_install\TMS470_docs\PlatformDevice\TMS570\Corona\ECC99_R4\ECC99.c
8/23/2012 5:47:21 PM
ECC_ODD_Ptr ->RAMCTRL_UN.RAMCTRL_ST.ECCWRITE_ENABLE = 0x1;
// Corrupt two bit error in OPCODE corresponding ECC Location
#if defined(_TMS470_BIG) || defined(__big_endian__)
temp_ram_odd = (unsigned char *) 0x40010B;
asm(" nop ");
*temp_ram_odd = 0x0A;
#elif defined(_TMS470_LITTLE) || defined(__little_endian__)
temp_ram_odd = (unsigned char *) 0x400108;
asm(" nop ");
*temp_ram_odd = 0x0F;
#error "ERROR"
#endif
//Disable the ECC DETECT Enable of TCRAMW
// ECC non writable
ECC_ODD_Ptr ->RAMCTRL_UN.RAMCTRL_ST.ECCWRITE_ENABLE = 0x0;
if (*temp_ram_odd != 0x0A)
{
for(;;);
}
//Enable the ECC DETECT Enable of TCRAMW
ECC_ODD_Ptr -> RAMCTRL_UN.RAMCTRL_ST.ECCDETECT_ENABLE =0xA;
//Enable the ECC for TCRAMW
Enable_ECC();
I changed to 0x0A
I compared to 0x0A after WRITE
C:\cgtools_install\TMS470_docs\PlatformDevice\TMS570\Corona\ECC99_R4\ECC99_org.c
9/23/2010 2:47:18 AM
ECC_ODD_Ptr ->RAMCTRL_UN.RAMCTRL_ST.ECCWRITE_ENABLE = 0x1;
// Corrupt two bit error in OPCODE corresponding ECC Location
#if defined(_TMS470_BIG) || defined(__big_endian__)
temp_ram_odd = (unsigned char *) 0x40010B;
asm(" nop ");
*temp_ram_odd = 0x0F;
#elif defined(_TMS470_LITTLE) || defined(__little_endian__)
temp_ram_odd = (unsigned char *) 0x400108;
asm(" nop ");
*temp_ram_odd = 0x0F;
#endif
//Disable the ECC DETECT Enable of TCRAMW
// ECC non writable
ECC_ODD_Ptr ->RAMCTRL_UN.RAMCTRL_ST.ECCWRITE_ENABLE = 0x0;
//Enable the ECC DETECT Enable of TCRAMW
ECC_ODD_Ptr -> RAMCTRL_UN.RAMCTRL_ST.ECCDETECT_ENABLE =0xA;
//Enable the ECC for TCRAMW
Enable_ECC();